

REPORT ON GUEST LECTURE ON VLSI TOOLS

In order to help the enthusiastic students, Alumni of the College Mr. S Krishna Prasad, Lead Admin, Wipro, Pune delivered a lecture on “VLSI Tools” on 16th March 2016 to final B.Tech students of ECE.

The seminar was headed by the Mr. K. Murali HOD, ECE,. A total of 105 students have attended this seminar. During the three hours of exciting and informative lecture, Mr. S Krishna Prasad explained that “Electronic design automation (EDA), which also is referred to as electronic computer-aided design (ECAD), is a category of software tools for designing electronic systems such as integrated circuits and printed circuit boards. The tools work together in a design flow that chip designers use to design and analyze entire semiconductor chips. Since a modern semiconductor chip can have billions of components, EDA tools are essential for their design”.

He listed different EDA tools used in various stages of IC Design

- High-level synthesis (or behavioural synthesis, algorithmic synthesis) – high-level design description (e.g. in C/C++) is converted into RTL.
- Logic synthesis – translation of RTL design description (e.g. written in Verilog or VHDL) into a discrete netlist of logic gates.
- Schematic capture – For standard cell digital, analog, RF-like Capture CIS in Orcad by Cadence and ISIS in Proteus
- Layout – usually schematic-driven layout, like Layout in Orcad by Cadence, ARES in Proteus

Furthermore, being an engineer himself, he shared his own experiences and motivated the students. Mr. K. Murali, HOD, ECE hoped that Mr S Krishna Prasad’s lecture is more informative and students got some knowledge on VLSI Design tools and their usage etc.

EVENT PHOTOS:



Resource Person explaining the VLSI Design flow



Resource Person explaining various
EDA Tools used in VLSI Design



Students listening to the lecture

